



IFW 2633

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kannan Raj et al.	§	Group Art Unit:	2633
		§		
Serial No.:	09/839,023	§	Examiner:	Dalzid E. Singh
		§		
Filed:	April 20, 2001	§	Atty. Dkt. No.:	ITL.0462US
		§		(P9816)
For:	OPTICALLY INTERCONNECTING MULTIPLE PROCESSORS	§	Assignee:	Intel Corporation

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated November 15, 2005 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.0462US).

Respectfully Submitted,

December 7, 2005

Date

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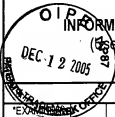
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Date of Deposit: December 7, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Monica Jacobs

 INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				ATTY DOCKET NO. ITL_0462US (P9816)		SERIAL NO. 09/839,023	
				APPLICANT(S): Kannan Raj et al.			
				FILING DATE: April 20, 2001		GROUP ART UNIT: 2633	
				U.S. PATENT DOCUMENTS			
*EXAMINER'S OFFICE INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	A.						
	B.						
	C.						
	D.						
	E.						
U.S. PATENT APPLICATION PUBLICATIONS							
	F.						
	G.						
	H.						
	I.						
	J.						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
/DS/	K.	EP 0 833 255	04/01/1998	Europe			
	L.						
	M.						
	N.						
	O.						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
/DS/	P.	M. Borella et al., <i>Optical Interconnects for Multiprocessor Architectures Using Wavelength-Division Multiplexing</i> , Proceedings of the Twenty-Seventh Hawaii International Conference, Los Alamitos, CA, Jan. 4, 1994, pgs. 499-508.					
	Q.						
	R.						
	S.						
	T.						
	U.						
EXAMINER /Dalzid Singh/ (01/08/2009)				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.